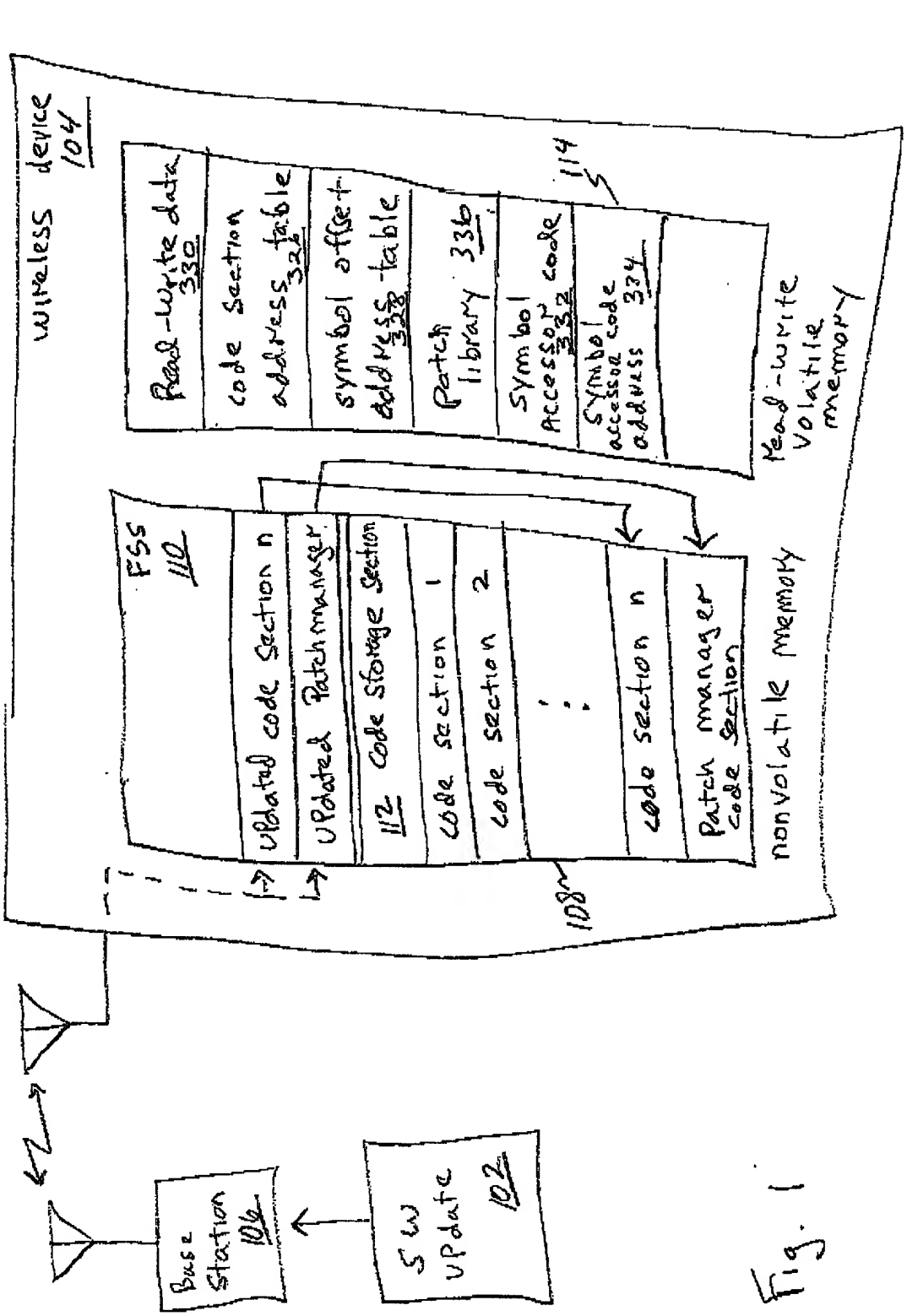
100
Wireless device 104
Read-write data 330
code section address table 326
symbol offset address table 328
Patch library 336
Symbol accessor code 332
Symbol accessor code address 334
114
Read-write Volatile memory
FSS 110
Updated code Section n
UPdated Patch manager
112 Code Storage Section
Code section 1
Code section 2
Code section n
Patch manager Code Section
nonvolatile memory
108
Base Station 106
SW UPdate 102

Fig. 1

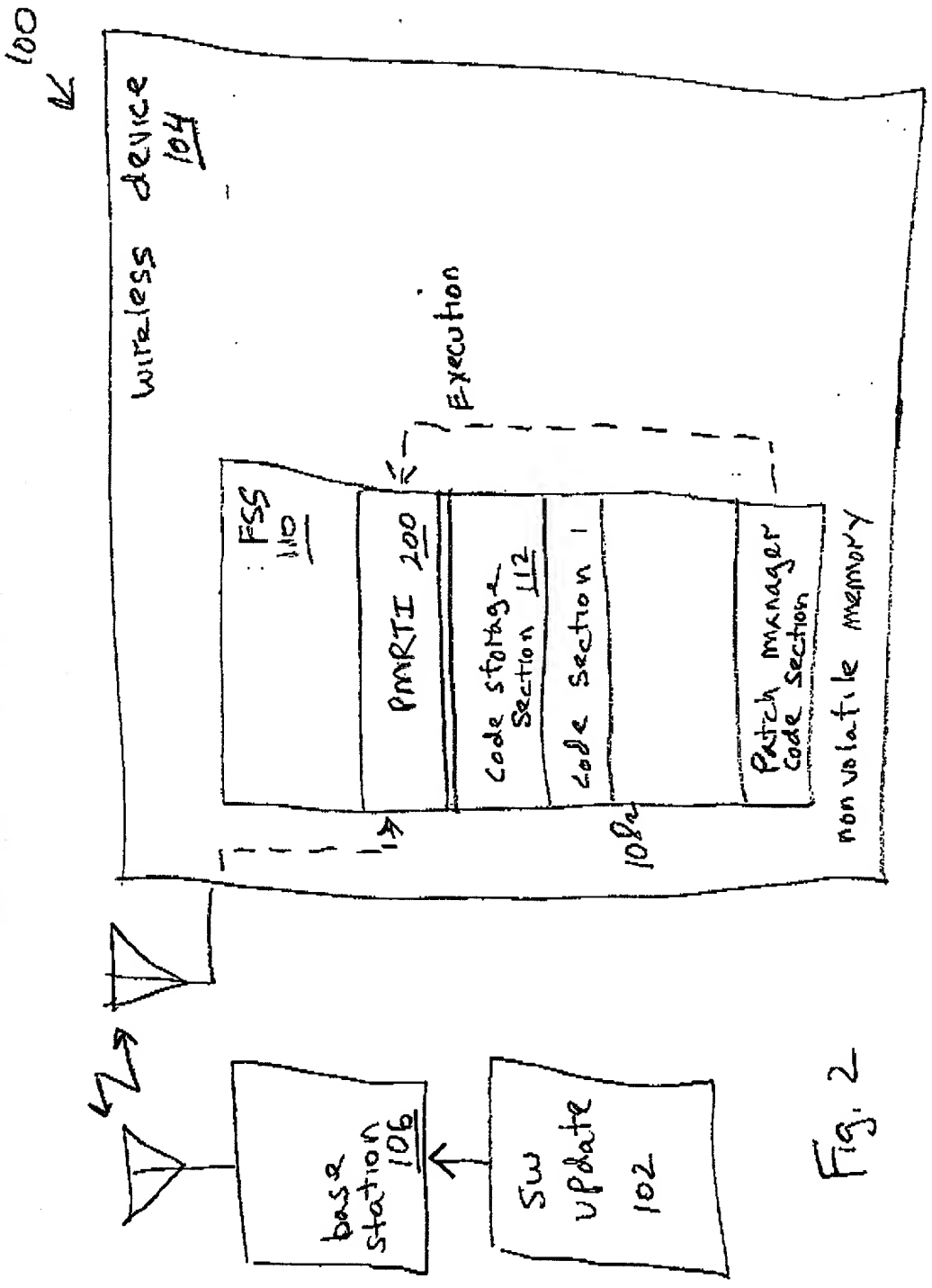
100
wireless device 104
FSS 110
PMRTI 200
Code storage Section 112
Code Section 1
Patch manager Code Section
Execution
108a
non volatile memory
base station 106
SW update 102

Fig. 2

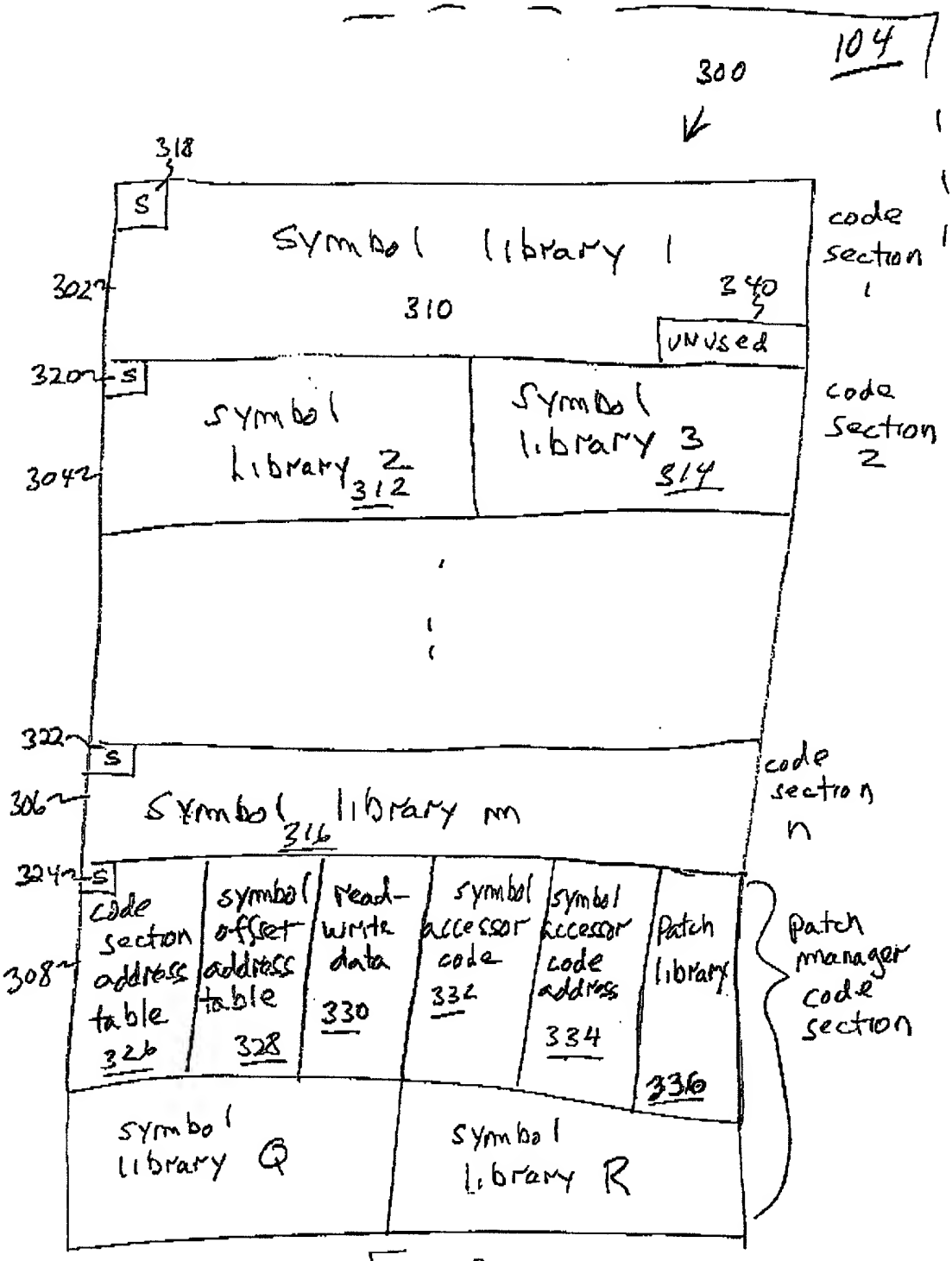
104
300
318
S
Symbol library 1
310
code section 1
302
340
UNUSED
320
S
Symbol library 2
312
Symbol library 3
314
code section 2
304
322
S
Symbol library m
316
code section n
306
324
S
code section address table
326
symbol offset address table
328
read-write data
330
symbol accessor code
332
symbol accessor code address
334
patch library
336
patch manager code section
308
Symbol library Q
Symbol library R

Fig. 3

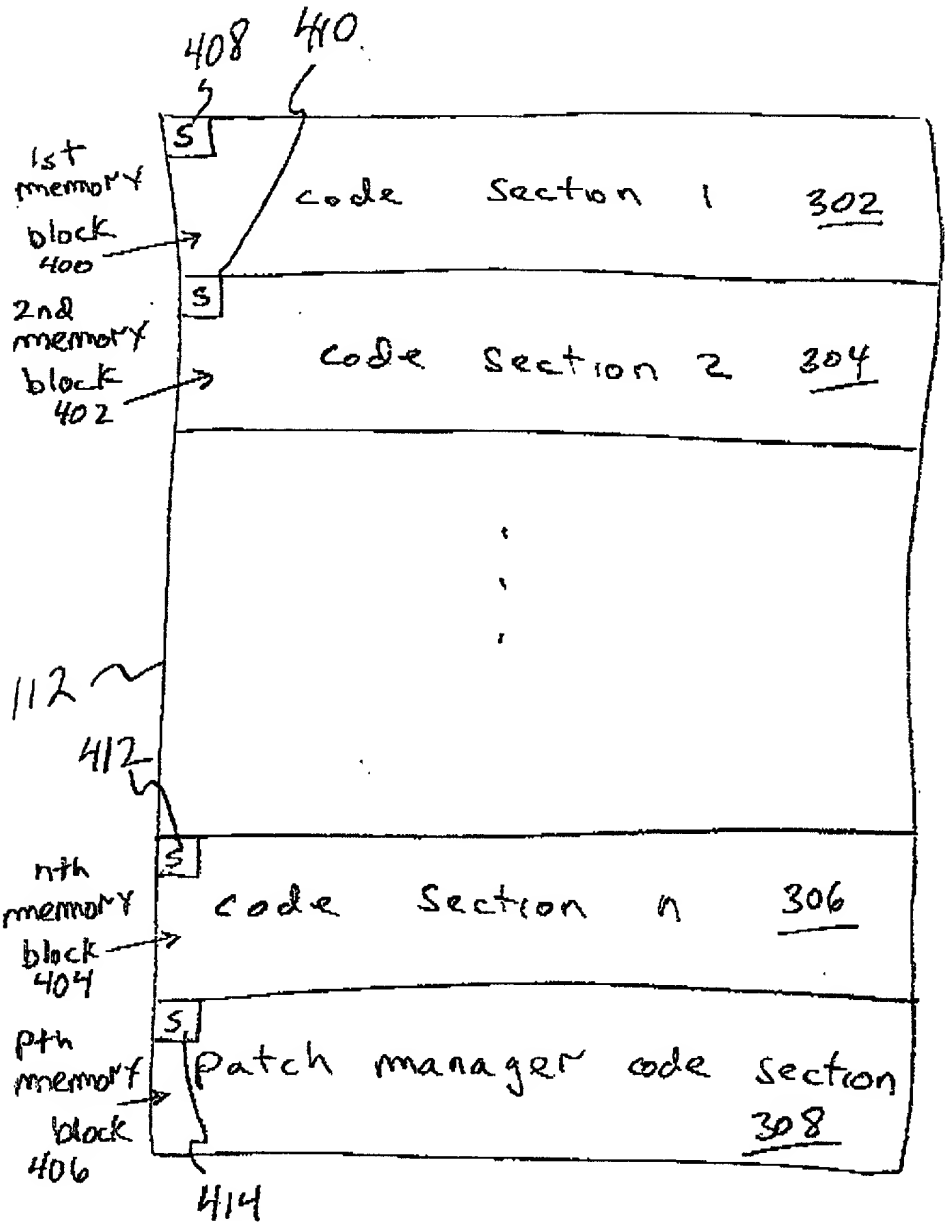
408
410
S
1st memory block 400
code Section 1 302
S
2nd memory block 402
code Section 2 304
112
412
S
nth memory block 404
code Section n 306
S
pth memory block 406
patch manager code section 308
414

Fig. 4

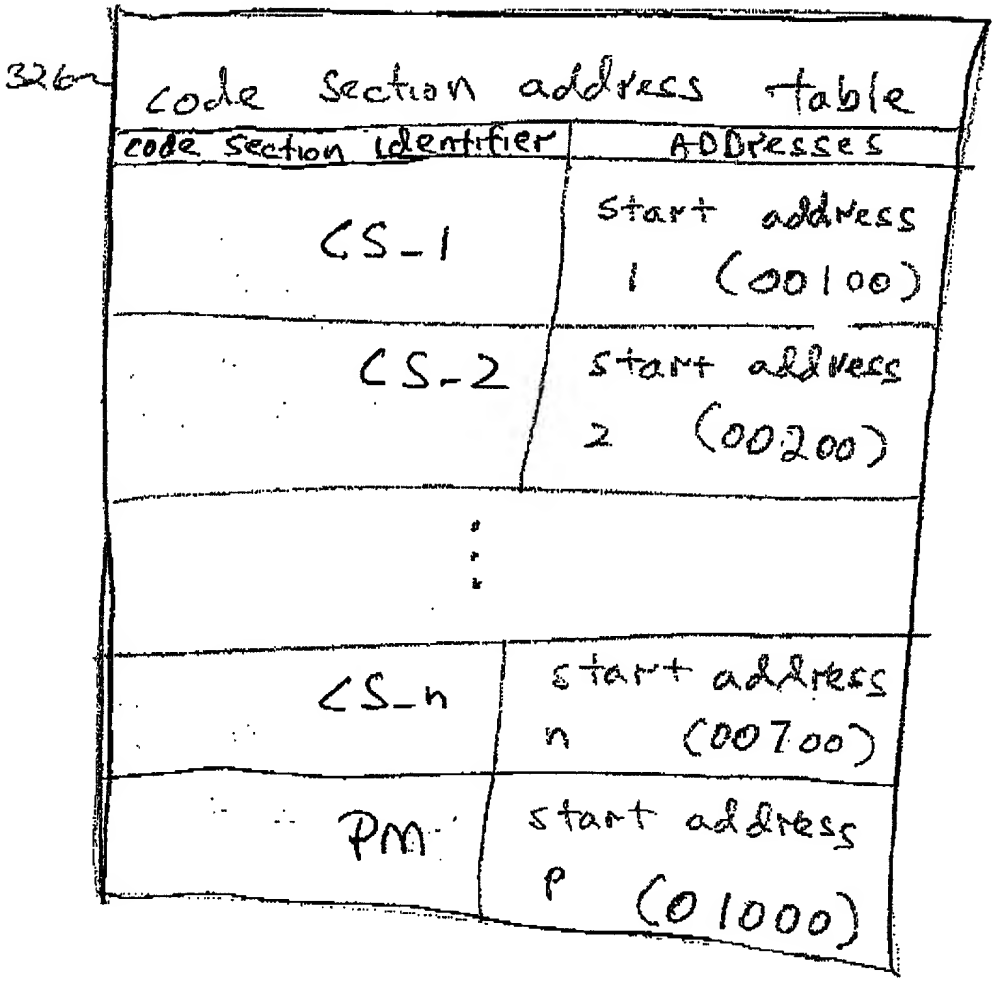
326
code Section address table
code Section Identifier
ADDresses
CS_1
start address 1 (00100)
CS_2
start address 2 (00200)
CS_n
start address n (00700)
PM
start address p (01000)

Fig. 5

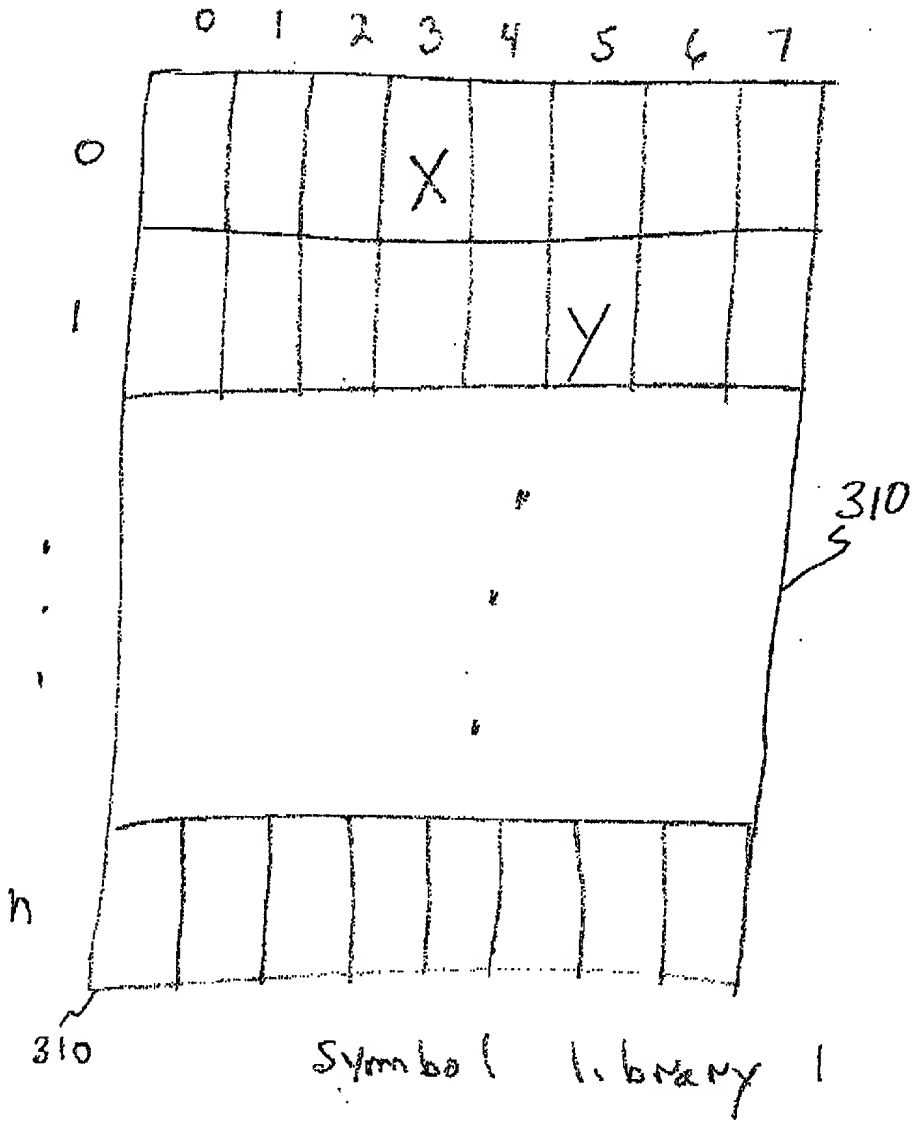

Fig. 6

Symbol offset address table

Symbol ID	code section ID	offset
X_1	CS_1	03
Y_1	CS_1	15
P_1	CS_2	11
Q_1	CS_2	33
AA_3	CS_2	47
⋮		

Fig. 7

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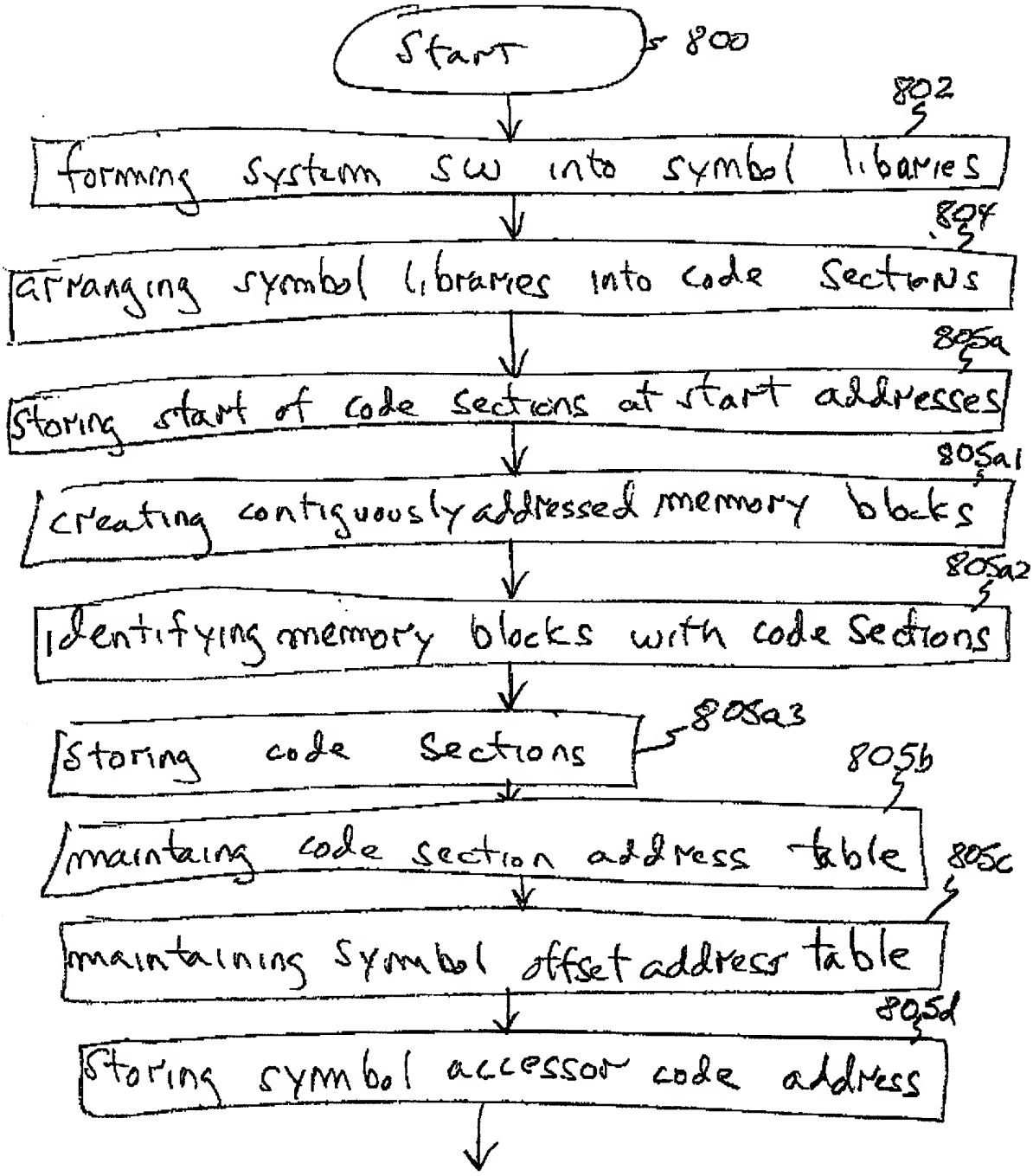

Fig. 8a to Fig. 8b

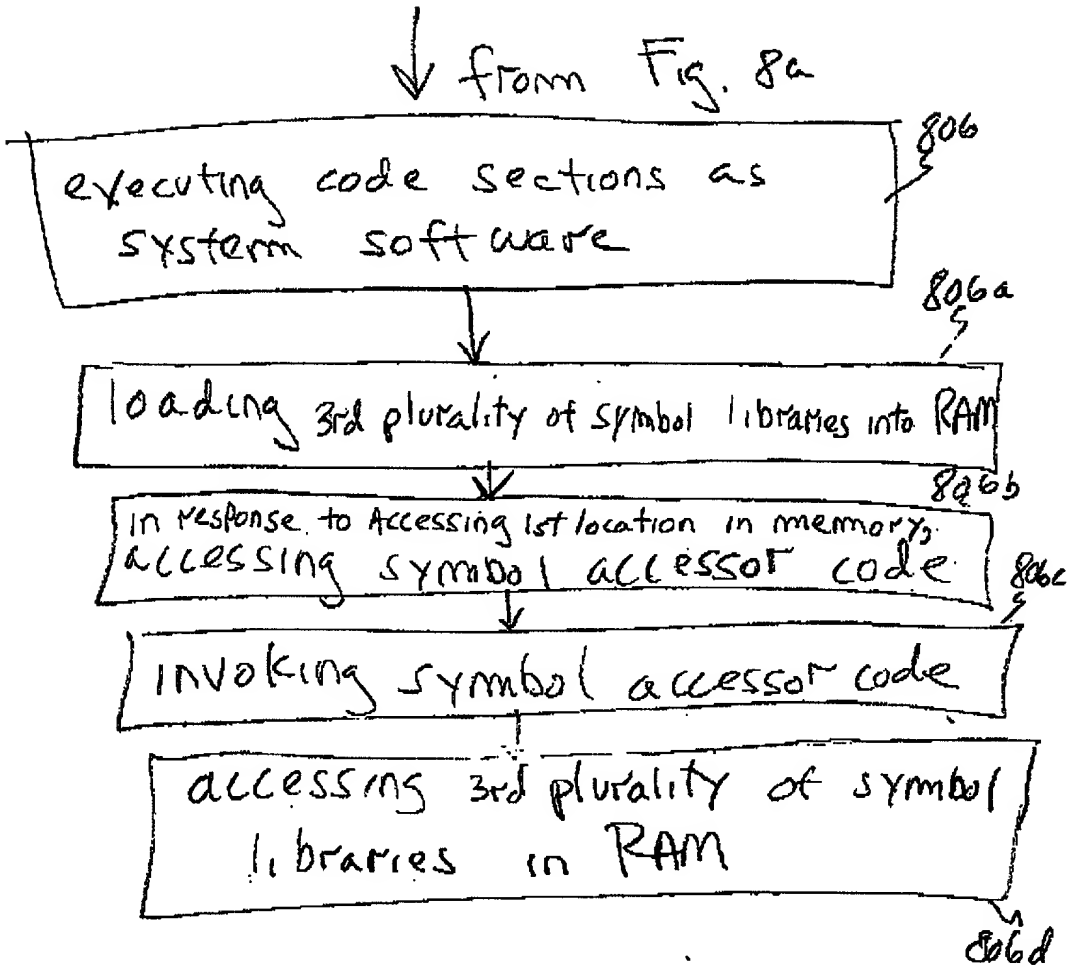
from Fig. 8a
executing code sections as system software
806
806a
loading 3rd plurality of symbol libraries into RAM
806b
in response to Accessing 1st location in memory, accessing symbol accessor code
806c
invoking symbol accessor code
accessing 3rd plurality of symbol libraries in RAM
806d

Fig. 8b